



TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))			Docket No. 15624-US
In Re Application Of: Farsheed Mahmoudi et al.			
Serial No. 10/725,513	Filing Date December 3, 2003	Examiner	Group Art Unit 2681
Title: LOW-VOLTAGE LOW-POWER HIGH-LINEARITY ACTIVE CMOS MIXER			
Address to: Assistant Commissioner for Patents Washington, D.C. 20231			
37 CFR 1.97 (b)			
1. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application; within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; or before the mailing date of a first Office Action on the merits, whichever event occurs last.			
37 CFR 1.97 (c)			
2. <input type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed after three months of the filing of a national application, or the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; or after the mailing date of a first Office Action on the merits, whichever occurred last but before the mailing date of either: 1. a Final Action under 37 CFR 1.113, or 2. a Notice of Allowance under 37 CFR 1.311, whichever occurs first. Also submitted herewith is: <input type="checkbox"/> a certification as specified in 37 CFR 1.97(e); OR <input type="checkbox"/> the fee set forth in 37 CFR 1.17(p) for submission of an Information Disclosure Statement under 37 CFR 1.97(c).			



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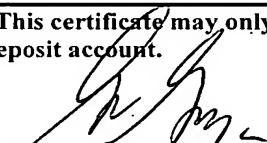
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37,547

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Substitute for form 1449B/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)		Complete if Known			
		Application Number	10/725,513		
		Filing Date	December 3, 2003		
		First Named Inventor	Farsheed Mahmoudi		
		Art Unit	2681		
		Examiner Name			
Sheet	2	of	2	Attorney Docket Number	15624-US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		KIM, Tae Wook et al., "Highly linear RF CMOS amplifier and mixer adopting MOSFET transconductance linearization by multiple gated transistors", IEEE, 2003, pages 107-110.	
		KIM, Youngwook et al., "Linearized mixer using predistortion technique", IEEE Microwave and Wireless Components Letters, Vol. 12, No. 6, 2002, pages 204-205.	
		Bautista, Edwin E. et al., "A high IIP2 downconversion mixer using dynamic matching", IEEE Journal of Solid-State Circuits, Vol. 35, No. 12, 2000, pages 1934-1941.	
		LI, Qiang et al. , "CMOS RF mixer no-linearity design", IEEE, 2001, pages 808-811.	
		WANG, Zhenhua et al., "A voltage-controllable linear MOS transconductor using bias offset technique", IEEE Journal of Solid-State Circuits, Vol. 25, No. 1, 1990, pages 315-317.	
		HEYDARI, Payam, "High-frequency noise in RF active CMOS mixers".	
		ROFOUGARAN, A. et al., "A1 GHz CMOS RF front-end IC with wide dynamic range".	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.
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